

## Enphase 6mA Declaration

Per the regulation *NEN 1010:2015, 712.530.3.4 Devices for earth leakage protection*, it is mandatory for a PV solar system that *where a residual current device is used for the protection of the PV-AC supply chain, this device for earth leakage protection must be of type B according to NEN-EN-IEC 62423, unless:*

- *the inverter provides at least a single separation between the AC side and the DC side,*
- *the installation provides at least a single separation between the inverter and the earth leakage protection device by means of separate windings of a transformer or*
- *according to a manufacturer's declaration, the inverter does not need a type B residual-current device.*

The Enphase Microinverters to which this declaration applies use a triple insulated High Frequency (HF) isolation transformer between the AC and DC circuits. This HF transformer effectively provides galvanic isolation between the DC stored energy and the AC output stage. When the AC semiconductors open, the DC is isolated from AC. The HF transformer is also triple insulated providing reinforced isolation

Apart from the internal HF transformer, which inherently eliminates DC leakage current, the Enphase system is also designed with fully double insulated chassis. This means DC and AC leakage is absent, creating a much safer situation, and removing the need for grounding within the Enphase system.

Additionally, the Q-Relay provides a second level protection by continuously monitoring DC Current Injection (DCI). The relay will trip if DCI crosses the threshold programmed into it. Enphase microinverters are designed, and type tested, to ensure DCI is never an issue.

**This declaration applies to all Enphase Microinverters that are part of the IQ-Series. The IQ-Series includes but is not limited to the IQ6-Series, IQ7-Series and the future IQ8-Series Microinverters. This includes future “Encharge” storage solutions that have integrated IQ8-Series Microinverters, and AC-Modules integrated with IQ-Series Microinverters.**



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